



Patent No.: 6,993,735

10/623,067

DA/ TAG

CERTIFICATION OF MAILING OR TRANSMISSION

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Werner H. Stermer

November 16, 2006
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : 6,993,735 B2
Patentee : Chee Hong Eric Liau
Issued : January 31, 2006
Title : Method of Generating a Test Pattern for Simulating and/or
Testing the Layout of an Integrated Circuit
Customer No. : 24131

Hon. Commissioner for Patents
Alexandria, VA 22313-1450

REQUEST UNDER 37 CFR 3.81(b)

Sir:

This is in response to the communication from Decision & Certificate of Corrections Branch mailed on November 6, 2006.

- (A) A copy of the Notice of Recordation of Assignment Document, reflecting the Reel/Frame: 016868/0984. The Assignment was recorded on August 10, 2005.
- (B) Request for Certificate of Correction along with the fee was previously submitted on October 2, 2006.
- (C) Enclosed is the processing fee, under 37 CFR 1.17(i).

Respectfully submitted,



Werner H. Stermer (34,956)
Date: November 13, 2006
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11/21/2006 BABRAHA1 00000033 6993735

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Assignments on the Web > Patent Query

Patent Assignment Abstract of Title

NOTE: Results display only for issued patents and published applications. For pending or abandoned applications please consult USPTO staff.

Total Assignments: 1

Patent #: [6993735](#)

Issue Dt: 01/31/2006 Application #: 10623067 Filing Dt: 07/18/2006

Publication #: [US20040034838](#)

Pub Dt: 02/19/2004

Inventor: Eric Liau

Title: METHOD OF GENERATING A TEST PATTERN FOR SIMULATING AND/OR TESTING THE LAYOUT OF AN INTEGRATED CIRCUIT

Assignment: 1

Reel/Frame: [016868/0984](#)

Recorded: 08/10/2005

Pages: 2

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: [LIAU, ERIC](#)

Exec Dt: 08/08/2003

Assignee: [INFINEON TECHNOLOGIES AG](#)

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MUENCHEN, GERMANY 81669

Correspondent: LERNER AND GREENBERG

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Search Results as of: 11/09/2006 03:00
If you have any comments or questions concerning the data displayed, contact PRD / Assignments at 571-272-3350.
Web interface last modified: July 26, 2006 v.1.10

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Practitioner's Docket No. M&N-IT-466

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : US 6,993,735 B2
Patentee : Chee Hong Eric Liao
Issued : January 31, 2006
Examiner : Binh C. Tat
Title : Method of Generating a Test Pattern for Simulating and/or Testing
the Layout of an Integrated Circuit
Docket No. : M&N-IT-466
Customer No. : 24131

Commissioner for Patents

P.O. Box 1450,

Alexandria, VA 22313-1450

ATTENTION: Decision and Certificate of Correction Branch of the Patent Issue
Division

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR APPLICANT'S MISTAKE (37 C.F.R. 1.323)

1. It is noted that an error appears in this patent of a
() clerical () typographical (X) minor

Nature or character, as more fully described below. It occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination. A certificate of correction is requested.

2. Attached hereto, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.
3. The exact page and line number where the error occurs in the application file are:

The inventor's full name was inadvertently omitted in the entire application and patent process.

4. Please send the Certificate to:

Name : LERNER GREENBERG STEMER LLP

Address : P.O. BOX 2480, HOLLYWOOD, FL 33022-2480

5. Please pay the fee of \$ 100.00, as required by 37 CFR 1.20(a), as follows:

X Enclosed is a credit card form for \$100.00.

Werner H. Stemer
WERNER H. STEMER (REG. NO. 34,956)

Oct. 2, 2006
DATE

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : US 6,993,735 B2
ISSUED : January 31, 2006
INVENTOR(S) : Chee Hong Eric Liao

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

Item [75] should read as follows:

Chee Hong Eric Liao, München (D)

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,993,735

No. of additional copies



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